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By: Wm. Stemer

Date: September 9, 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
Before the Board of Patent Appeals and Interferences

Inventor : Thomas Huttner et al. ✓
Applic. No. : 09/313,424 ✓
Filed : May 17, 1999 ✓
Title : SOI Semiconductor Configuration and Method of Fabrication the Same
Examiner : Brook Kebede Art Unit: 2823

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Notice of Appeal
L. McInnis
9/16/02

NOTICE OF APPEAL FROM THE PRIMARY EXAMINER TO THE
BOARD OF PATENT APPEALS AND INTERFERENCES

Hon. Commissioner of Patents and Trademarks,
Washington, D. C. 20231

Sir:

Applicants hereby appeal to the Board of Patent Appeals and Interferences from the decision dated May 9, 2002 of the Primary Examiner finally rejecting claims 16-20 and 24.

The \$320.00 appeal fee is enclosed. Petition for extension is herewith made. The extension fee for response within a period of one month pursuant to Section 1.136(a) in the amount of \$110.00 in accordance with Section 1.17 is enclosed herewith.

The Patent and Trademark Office is hereby given authority to charge Deposit Account No. 12-1099 of Lerner and Greenberg, P.A. for any fees due or deficiencies of payments made for any purpose during the pendency of the above-identified application.

Respectfully submitted,

Wm. Stemer

For Applicants

WERNER H. STEMER
REG. NO. 34,956

Date: September 9, 2002

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